

Configuration and Security bits

☐ 7 ☐ 6 ☐ 5 ☐ 4 ☐ 3 ☐ 2 ☐ Lock2 ☐ Lock1

☐ 7 ☐ 6 ☐ 5 ☐ SELFPRGEN ☐ DWEN ☐ BODLEVEL1 ☐ BODLEVEL0 ☐ RSTDISBL

☐ SPIEN ☐ EESAVE ☐ WDTON ☐ CKDIV8 ☒ SUT1 ☒ SUT0 ☒ CKSEL1 ☐ CKSEL0

☒ Checked items means programmed (bit = 0) ☐ UnChecked items means unprogrammed (bit = 1)

Refer to device datasheet, please

PONY

CodeVisionAVR Chip Programmer - STK200+/300

File Edit Program Read Compare Help

Chip: ATtiny13

FLASH

Start: 0 h End: 1FF h

Checksum: 0xFC00

EEPROM

Start: 0 h End: 3F h

Checksum: 0x3FC0

Chip Programming Options

FLASH Lock Bits

☒ No Protection
☐ Programming disabled
☐ Programming and Verification disabled

☐ Program Fuse Bit(s):

- ☐ CKSEL0=0
- ☒ CKSEL1=0
- ☒ SUT0=0
- ☒ SUT1=0
- ☐ CKDIV8=0
- ☐ WDTON=0
- ☐ EESAVE=0
- ☐ RSTDISBL=0
- ☐ BODLEVEL0=0
- ☐ BODLEVEL1=0
- ☐ SPMEN=0

CHIPBLASTER, CODEVISION